



# JX600J/JX600T

## for ARM Family



### Specifications



Common Part	
Target Processor	ARM7TDMI, ARM7TDMI-S, ARM9TDMI, ARM9E-S, ARM720T, ARM920T, ARM922T, ARM940T, ARM946E-S, ARM966E-S, ARM926EJ-S, TI - OMAP1510(ARM925T), OMAP161x(ARM926EJ-S) ALTERA – EXCALIBUR(ARM922T) MOTOROLA – <u>DragonBall</u> i.MX1(ARM920T) EPSON – (ARM720T)
Operation Voltage	1.7 v – 3.6 v
Operating Clock	Up to maximum clock frequency is supported, which differs depending on an MPU.
Memory Space	All memory space is released to a user's system
JTAG Clock	Auto/Manual setting (3KHz/25KHz/100KHz/1.5MHz/3MHz/6.25MHz/12.5MHz/25MHz)
Interrupts	All memory space is released to a user's system
Endian	Little Endian/Big Endian are supported
Target System I/F	JTAG I/F (20pin, 2.54mm pitch) 20pin JTAG Connector, same as Multi-ICE (ARM Ltd.) 14pin Connector (Option), same as Embedded-ICE ETM I/F (38pin, 0.64mm pitch) Straight type, GND lead 1.4mm, 2-767054-1 (AMP) Straight type, GND lead 2.74mm, 767054-1 (AMP) Straight type, GND lead 3.51mm, 767061-1 (AMP)
Software Break	Point break: 1024 points by replacing instructions with software break Temporary break: 1 point (by using On-Chip Resource) Countable break: 1 point
Hardware Break	2 points (Event break with ETM Resource)
OCD Break	Max. 2 points (Fetch break and Data access break in ROM area are supported.)
Flash Programming	Flash memory programmable with the standard commands (block erasing/programming) of JEDEC (compliant) & INTEL (equivalent) methods is supported.

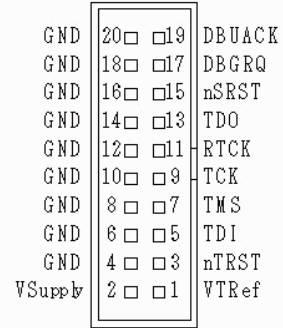
Other Specifications	
Host PC and OS	PC/AT-compatible, Microsoft Windows 98SE, Me, XP, NT4.0, 2000 USB (Full-speed)
Debugger	<u>microVIEW-PLUS</u>
Compiler	ARM SDT/ADS compiler Green Hills C/C++ compiler

### System Configuration

Model	Details
1	JX600J Main unit for JTAG Model, JTAG Probe, Install Kit (Debugger, USB cable, Documents)
	JX600T Main unit for Trace Model, AC Adaptor, JTAG/ETM Probe, Install Kit (Debugger, USB Cable, Documents)
	(Options)
	External Cable
	JTAG long cable JTAG 20pin, 30cm length

### JTAG Pin Assignment (20pin)

Pin No.	Signal Name	Target I/O	notes	Pin No.	Signal Name	Target I/O	notes
20	GND	-	-	19	DBGACK	OUT	*1
18	GND	-	-	17	DBGRRQ	IN	*1
16	GND	-	-	15	nSRST	O/D	*2
14	GND	-	-	13	TDO	OUT	-
12	GND	-	-	11	RTCK	OUT	*4
10	GND	-	-	9	TCK	IN	-
8	GND	-	-	7	TMS	IN	-
6	GND	-	-	5	TDI	IN	-
4	GND	-	-	3	nTRST	O/D	*2
2	VSupply	-	*3	1	VTRef	-	*3



I/O direction is the input-output direction viewed from the user's system side.

- Input: JX600 advicePOCKET → User's system
- Output: User's system → JX600 advicePOCKET
- O/D: Open Drain signal, bi-direction signal

\*1 There may be assigned depend on the MPU.

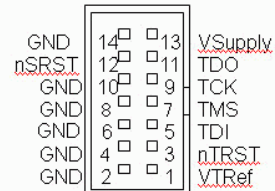
\*2 O/D. Be WIRED-OR the signal on the user's system.

\*3 Connect to the power supply on the user's system.

\*4 The loop back signal of TCK.

### JTAG Pin Assignment (14pin)

Pin No.	Signal Name	Target I/O	notes	Pin No.	Signal Name	Target I/O	notes
14	GND	-	-	13	VSupply	-	*2
12	nSRST	O/D	*1	11	TDO	OUT	-
10	GND	-	-	9	TCK	IN	-
8	GND	-	-	7	TMS	IN	-
6	GND	-	-	5	TDI	IN	-
4	GND	-	-	3	nTRST	O/D	*1
2	GND	-	-	1	VTRef	-	*2



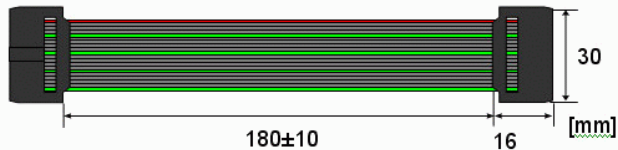
I/O direction is the input-output direction viewed from the user's system side.

- Input: JX600 advicePOCKET → User's system
- Output: User's system → JX600 advicePOCKET
- O/D: Open Drain signal, bi-direction signal

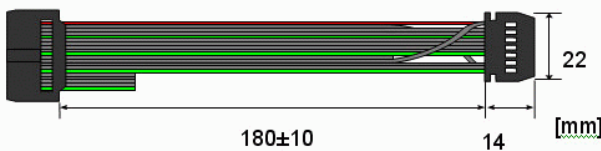
\*1 O/D. Be WIRED-OR the signal on the user's system.

\*2 Connect to the power supply on the user's system.

### JTAG Probe Dimension (20pin)

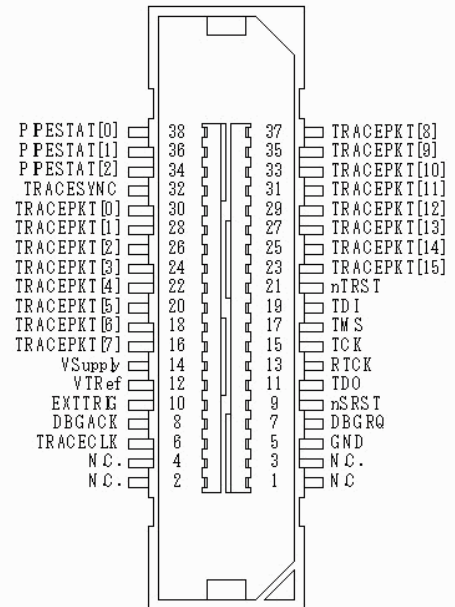


### JTAG Probe Dimension (14pin)



## ETM Pin Assignment (38pin)

Pin No.	Signal Name	Target I/O	notes	Pin No.	Signal Name	Target I/O	notes
38	PIPESTAT[0]	Output	-	37	TRACEPKT[8]	Output	*1*2
36	PIPESTAT[1]	Output	-	35	TRACEPKT[9]	Output	*1*2
34	PIPESTAT[2]	Output	-	33	TRACEPKT[10]	Output	*1*2
32	TRACESYNC	Output	-	31	TRACEPKT[11]	Output	*1*2
30	TRACEPKT[0]	Output	-	29	TRACEPKT[12]	Output	*1*2
28	TRACEPKT[1]	Output	-	27	TRACEPKT[13]	Output	*1*2
26	TRACEPKT[2]	Output	-	25	TRACEPKT[14]	Output	*1*2
24	TRACEPKT[3]	Output	-	23	TRACEPKT[15]	Output	*1*2
22	TRACEPKT[4]	Output	*2	21	nTRST	O/D	*4
20	TRACEPKT[5]	Output	*2	19	TDI	Input	-
18	TRACEPKT[6]	Output	*2	17	TMS	Input	-
16	TRACEPKT[7]	Output	*2	15	TCK	Input	-
14	VSupply	-	*5	13	RTCK	Output	*6
12	VTRef	-	*5	11	TDO	Output	-
10	EXTTRIG	Input	*3	9	Nsrst	O/D	*4
8	DBGACK	Input	*3	7	DBGRQ	Input	*3
6	TRACECLK	Output	-	5	GND	-	-
4	N.C.	-	-	3	N.C.	-	-
2	N.C.	-	-	1	N.C.	-	-



I/O direction is the input-output direction viewed from the user's system side.

- Input: JX600 advicePOCKET → User's system
- Output: User's system → JX600 advicePOCKET
- O/D: Open Drain signal, bi-direction signal

\*1 In case of 8 TRACEPKT, leave it unconnected or connect to GND.

\*2 In case of 4 TRACEPKT, leave it unconnected or connect to GND.

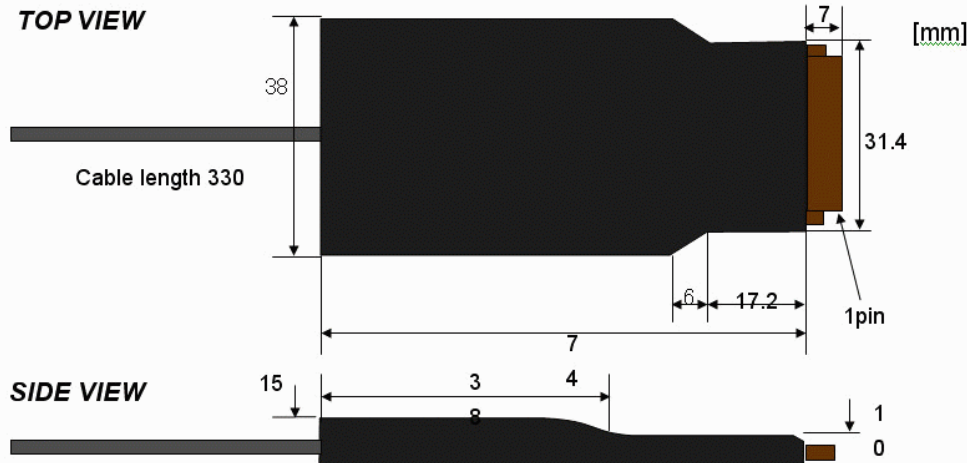
\*3 It may not be assigned depending on an MPU.

\*4 O/D. Wired-or with the signal on the user's system.

\*5 Connect to the power supply on the user's system.

\*6 The loop back signal of TCK.

## ETM Probe Dimension



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