

Specifications

Item	Specifications
Input unit	RF input Minimum pulse width: 10 ns Input voltage range: 0.1 Vp-p to 5 Vp-p (with conventional equalizer and AGC off) 0.1 Vp-p to 0.7 Vp-p (with conventional equalizer or AGC on) 0.1 Vp-p to 2.0 Vp-p (with limit equalizer on) Input coupling: AC, DC
	Trigger AUTO: Auto-slicer (10 kHz) AUTO + MANUAL: Trigger setting range: AUTO + setpoint (CODE setting -1000: approximately -2.2 V to +1000: approximately +2.2 V) MANUAL: Trigger setting range: CODE setting (-1000: approximately -2.2 V to +1000: approximately +2.2 V) When MANUAL is selected with pulse width measurement, equalizer, and AGC off Trigger setting range: ± 2 V (1 mV steps) Trigger setting accuracy: $\pm(10$ mV + 4% of setting) (When limit equalizer is used, "AUTO + MANUAL" and "MANUAL" cannot be used.)
	Arming input Setting: Select from the following: Internal, External \uparrow , External \downarrow Input: $Z_{in} = 10$ k Ω , TTL level Arming delay: 0.0 to 100.0 ms (0.1 ms steps)
Measurement items	Inhibit input Setting: Select from the following: OFF, POS, NEG Input: $Z_{in} = 10$ k Ω , TTL level Inhibit effective time: 0.1 ms to 100 ms
	Data-to-clock phase difference jitter and average values Measurement range: 0 to 20%, 0 to T ns (T: clock period) Trigger settings: \uparrow , \downarrow , \uparrow & \downarrow 2T exclusion function: Function for data-to-clock jitter measurement excluding edges before and after 2T data Pulse width jitter and average values (window range LEFT and RIGHT may be set as desired) Window setting range: 0.00 to 999.99 ns (0.01 ns steps)
	Level measurement Measurement function: ON/OFF setting Measurement range: 100 mVp-p to 2 Vp-p (3 mVp-p resolution) Measurement accuracy: $\pm(5\% + 10$ mV) (amplitude 1 Vp-p, 100 kHz sine wave measurement)
Display	Analog meter Display: Jitter σ (s), jitter ratio σ/T (%) Jitter ratio scale: Switch between 10% and 20% scale Jitter scale: Select from the following: 0.5 ns, 1.0 ns, 5.0 ns, 10 ns, 50ns, 0.1 μ s, 0.5 μ s, 1.0 μ s, 5.0 μ s
	7-segment LED display Display: Measurement values (jitter σ , jitter ratio σ/T , average value AVE, clock period T, number of samples Snum, level measurement Level) and settings Display ranges: Jitter ratio 0% to 25%, jitter 0 to 99.999 ns
	Dot matrix display: Set parameters, scale range GO/NO-GO LED display: Green (GO), Red (NO-GO) Judgment parameter is jitter σ or jitter ratio σ/T .
Measurement update rate	With DtoC high-speed calculation function off: 50 ms (Gate: 30 milliseconds, during data-to-clock jitter measurement: Both data edges) With DtoC high-speed calculation function on: 2 ms
Gate time	Setting range: With DtoC high-speed calculation function off: 1 ms to 1000 ms (1 ms steps) With DtoC high-speed calculation function on: 2 ms to 1000 ms (2 ms steps)
Block sampling	Set number of blocks: 1 to 99 (steps of 1) The maximum numbers of blocks that can be set are as follows: With DtoC high-speed calculation function off: (5 seconds / gate time) or 99, whichever is less With DtoC high-speed calculation function on: (1 second / gate time) or 99, whichever is less

Model and suffix codes

Model	Suffix code	Description
704610	-BD1	BD conventional equalizer, D-to-C high-speed calculation
Power cord specifications	-D	UL/CSA standard
	-F	VDE standard
	-Q	BS standard
	-R	AS standard
	-H	GB standard
Optional specifications	/LEQ	Limit equalizer option

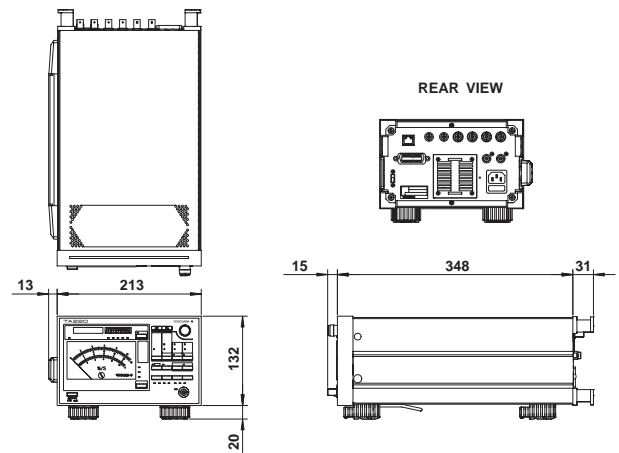
Item	Specifications
Equalizer	Conventional equalizer circuit (conforms to Blu-ray Disc RE standard version 1.0) Frequency characteristic: 16.5 MHz: +5.8 dB \pm 0.3 dB (amplitude ratio using 100 kHz as reference) Maximum group delay deviation: 1 nsp-p (typical; 3.0 MHz $\leq f \leq$ 22 MHz) Boost variable range: +3.0 dB to +9.0 dB (0.1 dB steps)
	Limit equalizer circuit (option /LEQ) (conforms to Blu-ray disc RE standard version 1.0) Frequency characteristic: 16.5 MHz: +5.8 dB \pm 0.3 dB (amplitude ratio using 100 kHz as reference) Maximum group delay deviation: 2 nsp-p (typical; 3.0 MHz $\leq f \leq$ 22 MHz) Boost variable range: +3.0 dB to +9.0 dB (0.2 dB steps)
PLL clock regeneration	Type of signal which can be synchronized: 1-7 modulation signal equivalent to basic clock in range of 64 MHz to 68 MHz PLL characteristic: $f_n = 8$ kHz, $\zeta = 2.0$
	PLL hold: When set to ON, holds the oscillating frequency for the duration of the INHIBIT input time plus 220 μ s (typical). (Cannot be used with the limit equalizer.)
DC clamp function	When the DC clamp function is set to ON, the auto-slicer DC cutoff is set to 3 MHz for the INHIBIT input period. (Cannot be used with the limit equalizer.)
Rear panel I/O	Output connectors LEVEL DC OUT Output level: 0 V to +5 V DC (initial setting: 1 V/Vp-p), 600 Ω output Output accuracy: ± 10 mV (disabled when DtoC high-speed calculation function is on) JITTER DC OUT Output level: 0 V to +5 V DC (initial setting: 0.2 V/V%), 600 Ω output Output accuracy: ± 10 mV EQUALIZED OUT: 50 Ω output MONITOR OUT: 50 Ω output CLOCK OUT: 50 Ω ± 0.4 V SLICED RF OUT: 50 Ω ± 0.4 V
	Input connectors EXT ARM IN: DC 10 k Ω TTL INHIBIT IN: DC 10 k Ω TTL
	Store/Recall function: Stores and recalls up to seven settings.
Communication	GP-IB: IEEE Std. 488.2-1992 Ethernet: 10BASE-TX, 10BASE-T
	General specifications Rated supply voltage: 100 to 120 V AC, 200 to 240 V AC Rated supply frequency: 50/60 Hz Maximum consumed power: 150 VA External dimensions: Approximately 213 (W) \times 132 (H) \times 350 (D) mm (not including protruding parts) Weight: Approximately 5 kg

The performance values presented above are obtained after allowing the equipment to warm up under the reference operating conditions.

Reference operating conditions: Ambient temperature of 23°C \pm 5°C, ambient humidity of 50% \pm 10% RH, supply voltage within 1% of rating

External dimensions

(unit: mm)



NOTICE

- Before operating the product, read the instruction manual thoroughly for proper and safe operation.
- If this product is for use with a system requiring safeguards that directly involve personnel safety, please contact the Yokogawa sales offices.

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